

CURRICULUM VITAE

Iain Hunter

3 Eastern Terrace Mews
Brighton BN2 1EP
Tel: +44(0)1273 628 700
E:iain@calibrato.com

Profile

Electronics graduate with 16 years industrial experience including 7 years self-employment experience. I have proven communication and leadership skills, capable of working independently or within a team. I have gained a wide range of sub-100MHz analogue and digital design experience. I have experience in leading successful projects from conception to customer delivery. I also have experience documenting my work in internal company literature, patents and international conferences.

Major Skills

Analogue/Digital Hardware Design
Full Custom Mixed Signal ASIC design
FPGA programming in VHDL, VSLI design,
Embedded Microprocessor (PIC) programming using C.

Employment History

Jan 2003 – 2010

Freelance Electronic Engineer Freelance projects including Capacitive Sensor Systems at Sensatech Research and a Cadence/Tanner-Tools evaluation project at Philips Research Labs UK. Completed a full custom mixed signal ASIC design in .35micron CMOS for a small capacitive tachometer. Design of hardware for use in an Electrical-Impedance-Tomography (EIT) systems. Analogue and digital hardware design for interface between microcomputer and highly multiplexed sensor array. Software development for real world data capture and data-logging for use in the development of an EIT system.

March 2001 – August 2002

Senior Electronics Engineer. Philips Mobile Display Systems, Kobe, Japan. Designing both driver and “system-on-glass” electronics (**Poly-Si LSI**) for mass production **mobile displays** and aircraft cockpit displays. **Microprocessor** and high speed **FPGA** driver design for mobile phone LCD. I was part of a team that established a VLSI design centre for design for production of Poly-Si, LCD displays.

July 1997 – March 2001

Senior Research Scientist. Philips Research Laboratories (PRL), Redhill, UK. Responsible for **research** into Flat Displays Systems (**LCD, Plasma** and polymer-LED). Producing “**one-off**” **video system designs** to test new concepts. Conceptualising and **patenting** original approaches for design of Flat Displays systems. **Project leader** of **Active Matrix Polymer-LED** display project in the UK. I was Responsible for the project liaison with the Dutch Research Laboratories. I provided some “on-the-job” **training** for newly recruited graduates.

July 1995 – July 1997

Electronic Engineer. Si-Plan Electronics Research, Stratford-upon-Avon, UK. Design and Test engineer of **low-volume-production** electro-mechanical systems. These included medical systems, pneumatic and hydraulic **control systems, switched-mode-power-supplies** and **sensor/actuator interface** systems.

Education

PhD (incomplete¹):

Neural networks for the control of **mobile robots** – *Warwick University* 1991-1995

1st Degree: **BEng (Hons) Engineering Electronics Iii**, *Warwick University, 1991*

3 A levels: Pure & Applied Maths, Physics, Computer Science

9 O levels: Art, Computer Science, English Language, English Literature, Geography, Maths, Physics, Statistics, Technical Drawing

Other Career Achievements

Invited Speaker at three international conferences, 2000, 2001, 2002

Project Leader of a team that won the **Best Technical Achievement** award, PRL, 1999.

Author of more than 20 patents.,

Member of the Lecture Committee, PRL, 1999,2001.

Pupil mentor and judge in inter-school engineering design competitions 1999, 2000.

Interests

Languages. Japanese from Jan. 2000 to the present. Some German.

Art including music and painting,

Sports, Table Tennis.

Referees

Contact details given on request

Contact details given on request